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Dated: December 16, 2008
Electronic Signature for Kevin J. Canning: /Kevin J. Canning/

Docket No.: MWS-040RCE
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Mojdeh Shakeri *et al.*

Patent No.: 7433812 B2

Issued: October 7, 2008

For: BLOCK DIAGRAM MODELING

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Madam:

Upon reviewing the above-identified patent, Patentee noted a typographical error which should be corrected.

In the Claims:

At Column 8, line 3 (Claim 9), of the printed patent, please add "signals" after --output--.

The error was not in the application as filed by applicant; accordingly no fee is required.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: December 16, 2008

Respectfully submitted,

Electronic signature: /Kevin J. Canning/
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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PATENT NO. : 7433812 B2
APPLICATION NO. : 09/870,280
ISSUE DATE : October 7, 2008
INVENTOR(S) : Mojdeh SHAKERI *et al.*

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**At column 8, line 3 (Claim 9), of the printed patent, please add “signals” after
--output--.**

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Dated: December 16, 2008

Electronic Signature for Kevin J. Canning: /Kevin J. Canning/

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